



SASURIE COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi. Affiliated to Anna University, Chennai

Near NH544, Coimbatore Bypass, Near Vijayamangalam Tollgate, Tirupur 638056

NAAC DOCUMENTS

QUALITY INDICATOR FRAME WORK

CRITERION - 3

RESEARCH, INNOVATION & EXTENSIONS

SUBMITTED BY

IQAC

INTERNAL QUALITY ASSURANCE CELL

SASURIE COLLEGE OF ENGINEERING



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Ref: SCE / FEE /Students / RM / 2018 – 2019 / ODD

13.08.2018

CIRCULAR

In order to address the recent research issues in the field of multi-disciplinary Engineering areas and to overcome such research issues, Department of Electrical and Electronics Engineering in association with IQAC of our Institution, is planning to conduct two days workshop for the students of II, III and IV year of EEE on the title “**Integrated Circuit Design Research Techniques**” from 17.08.2018 to 18.08.2018. At the end of the programme, participation certificates will be issued to the participants.

Resource Person Details	G.Dinesh kumar, Manager, MAS Data Technologies, Coimbatore – 641004	Mr.Mathesh.D, CEO, MAS Data Technologies, Coimbatore – 641004
Venue	Lecture hall of II & III year EEE	


HoD/EEE


PRINCIPAL

Copy to:

1. Chairman & Secretary for information
2. Principal office
3. IQAC Co-Ordinator
4. Class In charges - II, III & IV-Year EEE
5. II, III & IV-Year EEE Students
6. EEE Notice Board
7. Department File


Dr.M.VIJAYAKUMAR ME., Ph.D.,
PRINCIPAL
SASURIE COLLEGE OF ENGINEERING,
Vijayamangalam - 638 056, Tirupur (Dt).



SASURIE COLLEGE OF ENGINEERING

Vijayamangalam, Tiruppur

(Approved by AICTE, New Delhi, Affiliated to Anna University, Chennai - 600025)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

in association with IQAC

Cordially invites

Two days Research Methodology Programme (RM)

On

“Integrated Circuit Design Research Techniques”

Date : 17.08.2018 to 18.08.2018

Venue: Lecture hall of II & III year EEE

Resource Persons

Mr. G. Dineshkumar,
Manager,
MAS Data Technologies,
Coimbatore-641004.

Mr. D. Mathesh,
CEO,
MAS Data Technologies,
Coimbatore-641004.

in the esteemed presence of

Dr. R. Radhakrishnan

Principal

Shri A.M. Kandaswami
Chairman

Ms. K. Savitha Moganraj
Secretary

Dr. M. VIJAYAKUMAR ME., Ph.D.,
PRINCIPAL



SASURIE COLLEGE OF ENGINEERING,
Vijayamangalam - 638 056, Tiruppur (Dt).

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Students Participation List – Research Methodology Programme

“Integrated Circuit Design Research Techniques”

From 17.08.2018 to 18.08.2018

Academic Year : 2018 - 2019 /ODD

S.No.	Reg No.	Name of the Student	Year / Branch
1.	732417105002	ANJANA S	II/EEE
2.	732417105004	BARANIDHARAN P	II/EEE
3.	732417105006	KALEESWARAN P	II/EEE
4.	732417105007	KEERTHANA G	II/EEE
5.	732417105008	MALATHI S R	II/EEE
6.	732417105009	MARIA AROCKIYAM D	II/EEE
7.	732417105010	PRAKASII M	II/EEE
8.	732417105011	RAMESH KUMAR T	II/EEE
9.	732417105012	SATHISHKUMAR R	II/EEE
10.	732417105013	SEDHUMADHAVAN A	II/EEE
11.	732417105014	SHANMUGAM S	II/EEE
12.	732417105015	SOUNDARYA T	II/EEE
13.	732417105016	SREEVENI S	II/EEE
14.	732417105019	VIGNESH S	II/EEE
15.	732417105701	SEVVANDHI D	II/EEE
16.	732417105702	RANJITH C	II/EEE
17.	732416105001	AMSAVENI S	III/EEE
18.	732416105002	ARIHARAN P	III/EEE
19.	732416105003	BASKAR S	III/EEE
20.	732416105004	BOOPATHI S	III/EEE
21.	732416105005	DHARANI D	III/EEE
22.	732416105006	HARISH D	III/EEE
23.	732416105007	KALLALAIHAR K S	III/EEE
24.	732416105009	KARTHIKEYAN V	III/EEE
25.	732416105010	KIRUBHAKARAN R	III/EEE
26.	732416105011	NAVEENKUMAR M	III/EEE
27.	732416105012	NAVEENKUMAR R	III/EEE
28.	732416105013	PAVITHRA M	III/EEE
29.	732416105014	RAJESHKUMAR M	III/EEE
30.	732416105015	TAMILSELVAM G	III/EEE

M

Students Participation List – Research Methodology Programme

S.No.	Reg No.	Name of the Student	Year / Branch
31.	732416105016	VANMATHI P	III/EEE
32.	732416105017	VIDHIYA V	III/EEE
33.	732416105301	AJITH M	III/EEE
34.	732416105302	GUNASEKARAN S	III/EEE
35.	732416105501	CHANDHIRAKUMAR M	III/EEE
36.	732415105001	ATHIRA K S	IV/EEE
37.	732415105002	BHUVANESHWARI S	IV/EEE
38.	732415105003	DEEPA S	IV/EEE
39.	732415105004	DEVARAJ K R	IV/EEE
40.	732415105005	GOWSIKKUMAR A	IV/EEE
41.	732415105006	JANAKI PRIYA M	IV/EEE
42.	732415105007	MANIKANDAN S	IV/EEE
43.	732415105008	MANIMEGALAI S	IV/EEE
44.	732415105009	OVIYA S	IV/EEE
45.	732415105010	PAVITHRA B	IV/EEE
46.	732415105011	PAVITHRA V	IV/EEE
47.	732415105012	RAGU P	IV/EEE
48.	732415105013	RAJADURAI T	IV/EEE
49.	732415105014	RAMYA S	IV/EEE
50.	732415105015	SUGANYA R	IV/EEE
51.	732415105016	VALLINAYAKI K	IV/EEE
52.	732415105304	SHANMUGA SUNDARAM S	IV/EEE
53.	732415105501	MANIKANDAN K	IV/EEE

P. Manj
Event Coordinator

S. Seng
HoD/EEE

me
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PRINCIPAL



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Students Attendance List – Research Methodology Programme

“Integrated Circuit Design Research Techniques”

From 17.08.2018 to 18.08.2018

Academic Year : 2018 - 2019 / ODD

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1.	732417105002	ANJANA S	II/EEE	Anjana S
2.	732417105004	BARANIDHARAN P	II/EEE	Barani P
3.	732417105006	KALEESWARAN P	II/EEE	Kaleeswaran P
4.	732417105007	KEERTHANA G	II/EEE	Keerthana G
5.	732417105008	MALATHI S R	II/EEE	Malathi S R
6.	732417105009	MARIA AROCKIYAM D	II/EEE	Maria Arockiyam D
7.	732417105010	PRAKASH M	II/EEE	Prakash M
8.	732417105011	RAMESH KUMAR T	II/EEE	Ramesh Kumar T
9.	732417105012	SATHISHKUMAR R	II/EEE	Sathish Kumar R
10.	732417105013	SEDHUMADHAVAN A	II/EEE	Sedhumadhavan A
11.	732417105014	SHANMUGAM S	II/EEE	Shanmugam S
12.	732417105015	SOUNDARYA T	II/EEE	Soundarya T
13.	732417105016	SREEVENI S	II/EEE	Sreeveni S
14.	732417105019	VIGNESH S	II/EEE	Vignesh S
15.	732417105701	SEVVANDHI D	II/EEE	Sevvandhi D
16.	732417105702	RANJITH C	II/EEE	Ranjith C
17.	732416105001	AMSAVENI S	III/EEE	Amsaveni S
18.	732416105002	ARIHARAN P	III/EEE	Ariharan P
19.	732416105003	BASKAR S	III/EEE	Baskar S
20.	732416105004	BOOPATHI S	III/EEE	Boopathi S
21.	732416105005	DHARANI D	III/EEE	Dharani D
22.	732416105006	HARISH D	III/EEE	Harish D
23.	732416105007	KALLALAHAR K S	III/EEE	Kallalahar K S
24.	732416105009	KARTHIKEYAN V	III/EEE	Karthikeyan V
25.	732416105010	KIRUBHAKARAN R	III/EEE	Kirubhakaran R
26.	732416105011	NAVEENKUMAR M	III/EEE	Naveenkumar M
27.	732416105012	NAVEENKUMAR R	III/EEE	Naveenkumar R
28.	732416105013	PAVITHRA M	III/EEE	Pavithra M
29.	732416105014	RAJESHKUMAR M	III/EEE	Rajesh Kumar M


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Students Attendance List – Research Methodology Programme

S.No.	Reg No.	Name of the Student	Year / Branch	Signature
30.	732416105015	TAMILSELVAM G	III/EEE	Tamilselvam G
31.	732416105016	VANMATHI P	III/EEE	P. Vanmathi
32.	732416105017	VIDHYA V	III/EEE	Vidhya V
33.	732416105301	AJITH M	III/EEE	Ajith M
34.	732416105302	GUNASEKARAN S	III/EEE	Gunasekaran S
35.	732416105501	CHANDHIRAKUMAR M	III/EEE	Chandhirakumar M
36.	732415105001	ATHIRA K S	IV/EEE	Athira K S
37.	732415105002	BHUVANESHWARI S	IV/EEE	Bhuvaneshwari S
38.	732415105003	DEEPA S	IV/EEE	Deepa S
39.	732415105004	DEVARAJ K R	IV/EEE	Devaraj K R
40.	732415105005	GOWSSIKKUMAR A	IV/EEE	Gowssikkumar A
41.	732415105006	JANAKI PRIYA M	IV/EEE	Janaki Priya M
42.	732415105007	MANIKANDAN S	IV/EEE	Manikandan S
43.	732415105008	MANIMEGALAI S	IV/EEE	Manimegalai S
44.	732415105009	OVIYA S	IV/EEE	Oviya S
45.	732415105010	PAVITHRA B	IV/EEE	Pavithra B
46.	732415105011	PAVITHRA V	IV/EEE	Pavithra V
47.	732415105012	RAGU P	IV/EEE	Ragu P
48.	732415105013	RAJADURAIT	IV/EEE	Rajadurait
49.	732415105014	RAMYA S	IV/EEE	Ramya S
50.	732415105015	SUGANYA R	IV/EEE	Suganya R
51.	732415105016	VALLINAYAKI K	IV/EEE	Vallinayaki K
52.	732415105304	SHANMUGA SUNDARAM S	IV/EEE	Shanmuga Sundaram S
53.	732415105501	MANIKANDAN K	IV/EEE	Manikandan K

P. Manj.
Event Coordinator

S. Senthil
HOD/EEE

Me

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Vijayamangalam - 638 056, Tirupur (Dt).

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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“Integrated Circuit Design Research Techniques”

From 17.08.2018 to 18.08.2018

Academic Year : 2018 - 2019 / ODD

S.No.	Reg No.	Name of the Student	Year / Branch	17.08.2018		18.08.2018		Signature
				FN	AN	FN	AN	
1.	732417105002	ANJANA S	II/EEE	/	/	/	/	Anjana S
2.	732417105004	BARANIDHARAN P	II/EEE	/	/	/	/	Baranidharan P
3.	732417105006	KALEESWARAN P	II/EEE	/	/	/	/	Kaleeswaran P
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7.	732417105010	PRAKASH M	II/EEE	/	/	/	/	Prakash M
8.	732417105011	RAMESH KUMAR T	II/EEE	/	/	/	/	Ramesh Kumar T
9.	732417105012	SATHISHKUMAR R	II/EEE	a	a	a	a	Sathish Kumar R
10.	732417105013	SEDHUMADHAVAN A	II/EEE	/	/	/	/	Sedhumadhavan A
11.	732417105014	SHANMUGAM S	II/EEE	/	/	/	/	Shanmugam S
12.	732417105015	SOUNDARYA T	II/EEE	/	/	/	/	Soundarya T
13.	732417105016	SREEVENI S	II/EEE	/	/	/	/	Sreeveni S
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22.	732416105006	HARISH D	III/EEE	/	/	/	/	Harish D
23.	732416105007	KALLALAHAR K S	III/EEE	/	/	/	/	Kallalahar K S
24.	732416105009	KARTHIKEYAN V	III/EEE	/	/	/	/	Karthikeyan V
25.	732416105010	KIRUBHAKARAN R	III/EEE	/	/	/	/	Kirubhakaran R
26.	732416105011	NAVEENKUMAR M	III/EEE	/	/	/	/	Naveenkumar M
27.	732416105012	NAVEENKUMAR R	III/EEE	/	/	/	/	Naveenkumar R
28.	732416105013	PAVITHRA M	III/EEE	/	/	/	/	Pavithra M
29.	732416105014	RAJESHKUMAR M	III/EEE	/	/	/	/	Rajesh Kumar M


 Dr. M. VIJAYAKUMAR ME., Ph.D.,
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SASURIE COLLEGE OF ENGINEERING,
 Vijayamangalam - 638 056, Tirupur (Dt).

Students Attendance List – Research Methodology Programme

S.No.	Reg No.	Name of the Student	Year / Branch	17.08.2018		18.08.2018		Signature
				FN	AN	FN	AN	
30.	732416105015	TAMILSELVAM G	III/EEE	/	/	/	/	Tamilselvi
31.	732416105016	VANMATHI P	III/EEE	/	/	/	/	Vanathi
32.	732416105017	VIDHYA V	III/EEE	/	/	/	/	Vidhya V
33.	732416105301	AJITH M	III/EEE	/	/	/	/	Ajith M
34.	732416105302	GUNASEKARAN S	III/EEE	/	/	/	/	Gunasekaran S
35.	732416105501	CHANDHRAKUMAR M	III/EEE	/	/	/	/	Chandhrakumar M
36.	732415105001	ATHIRA K S	IV/EEE	/	/	/	/	Athira S
37.	732415105002	BHUVANESHWARI S	IV/EEE	/	/	/	/	Bhuvaneshwari S
38.	732415105003	DEEPA S	IV/EEE	/	/	/	/	Deepa S
39.	732415105004	DEVARAJ K R	IV/EEE	/	/	/	/	Devaraj
40.	732415105005	GOWSSIKKUMAR A	IV/EEE	/	/	/	/	Gowssikkumar A
41.	732415105006	JANAKI PRIYA M	IV/EEE	/	/	/	/	Janaki Priya M
42.	732415105007	MANIKANDAN S	IV/EEE	/	/	/	/	Manikandan S
43.	732415105008	MANIMEGALAI S	IV/EEE	/	/	/	/	Manimegalai S
44.	732415105009	OVIYA S	IV/EEE	/	/	/	/	Oviya S
45.	732415105010	PAVITHRA B	IV/EEE	/	/	/	/	Pavithra B
46.	732415105011	PAVITHRA V	IV/EEE	/	/	/	/	Pavithra V
47.	732415105012	RAGU P	IV/EEE	/	/	/	/	Ragu P
48.	732415105013	RAJADURAI T	IV/EEE	/	/	/	/	Rajadurai T
49.	732415105014	RAMYA S	IV/EEE	/	/	/	/	Ramyas
50.	732415105015	SUGANYA R	IV/EEE	/	/	/	/	Suganya R
51.	732415105016	VALLINAYAKI K	IV/EEE	/	/	/	/	Vallinayaki K
52.	732415105304	SHANMUGA SUNDARAM S	IV/EEE	/	/	/	/	Shanmuga Sundaram S
53.	732415105501	MANIKANDAN K	IV/EEE	/	/	/	/	Manikandan K

P. Mani
Event Coordinator

S. Suresh
HoD/EEE

Me

Dr.M.VIJAYAKUMAR ME., Ph.D.,
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Report on Research Methodology Programme

Title:	Integrated Circuit Design Research Techniques				
Resource Person:	G.Dinesh kumar, Manager, MAS Data Technologies, Coimbatore – 641004		Mr.Mathesh.D, CEO, MAS Data Technologies, Coimbatore – 641004		
Date of conduct From :	17.08.2018	To:	18.08.2018	Duration:	2 Days
Organized by :	ELECTRICAL AND ELECTRONICS ENGINEERING in association with IQAC of our Institution				
Academic Year:	2018 – 2019		Semester:	ODD	
Participant Year:	II, III, IV Year EEE		No. of Students Participated:	53	
Venue:	Lecture hall of II & III year EEE				

Outcome of Research Methodology Programme

At the end of the Research Methodology Programme, Students can be able to

- Acquire proficiency in selecting and applying appropriate research methodologies to address research questions and objectives in IC design.
- Gain insights into the theoretical and practical applications of research techniques within the realm of IC design.
- Enhance skills in IC modeling and simulation, employing software tools such as SPICE, Cadence Virtuoso, and Synopsys Design Compiler to analyze circuit performance and behavior.
- Explore strategies for designing and optimizing ICs, including transistor sizing, placement and routing, and power consumption analysis.
- Apply research techniques to investigate real-world problems and challenges in IC design, contributing to the advancement of knowledge and innovation in the field of microelectronics.

Glimpse of Research Methodology Programme Photos



Erode, Tamil Nadu, India
Sasurie College of Engineering, Sasurie College Nadupatti
Lat 11.236745°
Long 77.437135°
17/08/18 10:30 AM GMT +05:30

P. Manj
Event Co-ordinator

Srin
HoD/ EEE

crh
Principal

M

Dr.M.VIJAYAKUMAR ME., Ph.D.,
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Vijayamangalam - 639 056, Tirupur (Dt).



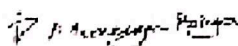
SASURIE COLLEGE OF ENGINEERING



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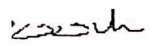
Vijyamangalam, Tiruppur

CERTIFICATE OF PARTICIPATION

This is to Certify that Ms. RAMYA S, IV / EEE
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.


IQAC Co-ordinator



SASURIE COLLEGE OF ENGINEERING,
Vijyamangalam - 638 056, Tiruppur (Dt).


Principal



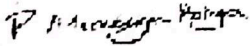
SASURIE COLLEGE OF ENGINEERING

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Vijyamangalam, Tiruppur

CERTIFICATE OF PARTICIPATION

This is to Certify that Mr. RAJADURAI T. IV / EEE
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.


IQAC Co-ordinator


Dr.M.VIJAYAKUMAR ME., Ph.D.,
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Vijyamangalam, Tiruppur

CERTIFICATE OF PARTICIPATION

This is to Certify that Mr . NAVEENKUMAR M, III / EEE
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.


IQAC Co-ordinator


Dr. W. VIJAYAKUMAR ME., Ph.D.,
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Principal



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Vijyamangalam, Tiruppur

CERTIFICATE OF PARTICIPATION

This is to Certify that **Mr . ARIHARAN P. III / EEE**
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.

P. S. Aravindhan
IQAC Co-ordinator



M.V.K.
Dr.M.VIJAYAKUMAR ME., Ph.D.,
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Vijyamangalam - 638 056, Tirupur (Dt).

V. Senthil
Principal



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CERTIFICATE OF PARTICIPATION

This is to Certify that Mr . RAMESH KUMAR T, II / EEE
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.


IQAC Co-ordinator


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Vijyamangalam - 638 058, Tirupur (Dt).


Principal




SASURIE COLLEGE OF ENGINEERING

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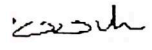
Vijyamangalam, Tiruppur

CERTIFICATE OF PARTICIPATION

This is to Certify that Mr . KALEESWARAN P, II / EEE
has participated in the Research Methodology Workshop on "Integrated Circuit Design Research Techniques" conducted by the Department of Electrical and Electronics Engineering in association with "MAS Data Technologies" and IQAC of Sasurie College of Engineering from 17.08.2018 to 18.08.2018 during the Academic year 2018-2019.


IQAC Co-ordinator


Dr.M.VIJAYAKUMAR ME., Ph.D.,
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