

SASURIE COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi. Affiliated to Anna University, Chennai

Near NH544, Coimbatore Bypass, Near Vijayamangalam Tollgate, Tirupur 638056

NAAC DOCUMENTS

QUALITY INDICATOR FRAME WORK

CRITERION - 1

CURRICULAR ASPECTS



INTERNAL QUALITY ASSURANCE CELL

SASURIE COLLEGE OF ENGINEERING





Criterion 1	Curricular Aspects	100
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1.1 Curricular Planning and Implementation (20)

1.1.1The Institution ensures effective curriculum planning and delivery through a well-planned and documented process including Academic calendar and conduct of continuous internal Assessment

Description S.No Contents - Course File 1 2 Time Table 3 Students Name List Subject Information Record 4 5 **Syllabus** 6 Lesson Plan Test Plan For Subject 7 8 Result Analysis of Test 9 Corrective Action Report 10 Quality Objective Monitoring Record 11 Assignment Schedule 12 Internal question paper 13 Internal assessment Test Answer book 14 Assignment Question Paper 15 Assignment Answer Sheet

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SASURIE College of Engineering

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and Electronics Engineering

Department Subject Code & Name Class & Batch	EE6601 & Solid : III 5	State Drives
Semester	: VI	

Semester	: VI CONTENTS – COURSE FILE	REMARKS
5.110	PARTICULARS	REIVIANNS
1	Time Table	
2	Student name list	and when the constrainty is a grown water on a second second second second second second second second second s
3	Student arrear list	
4	Subject Information Record	
5	Syllabus	
- 6	Lesson Plan	
7	Test Plan for the Subject	
8	Result Analysis	
9	Corrective Action Report	
10	Quality objective monitoring record	
- 11	Internal test mark sheet(Consolidated)	
12	Internal test question paper with answer key	
13	Model question paper with answer key	
14	Slip test question paper with answer key	
115	Sample Answer paper for all test(Min-3)	
16	Content beyond the syllabus	r
17	Tutorial Class – schedule and content	
18	Assignment – schedule and paper	
19	PPT - handout	anness and the second sec
20	Video - Animation - Soft copy	
20		
22	Ouestion bank Sample university question papers(min 5 QP-recent exam)	
and alarman 11		
23	Lecture Note Special Class if any, Approval letter, Schedule, content covered.	
24	Spark faculty score, student score	
25	Spark racuny score, store	

Approved By Prepared By S. DAA Sign. S. Deepika 113774 HD Faculty

Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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CLASS TIME TABLE - Academic Year 2018-19- EVEN Semester wet 1557 Pile Department fff Name Mis 5 Deepika Designation: Assistant Professor/TEE v v IV -1 11 HOUN 7.50p.m 10 12.35 p.m 10 10.40 s.m 8.584 m 100.0 m 14500 1 20 p m 10 1 05p m. 9 50 a.m 10.55 M.m 1145 am. 9 00 e m 10 10 10 10 100pm 10 1.70 pm DAY/ 10 10 10.55 p.m. 9 00a m 1450 m 4.50 m 1 50p.m TIME 12.35 p.m. 2 05 p.m 11 45 a m 9.50 a.m 10 40 a m 150 "S SESSION \$50 550 MOR PEGIAS PEDIAS 15.0 TUR 180 SSD 55D wib LUNCH 941.44 BINES BREAK SSD -UB TED -10 -55D \$\$0 -18.0 TED No of hours Subject Code Branch & Semester Name of the Subject S.No Acronym 7 Transmission & Distribution EEE & IV EE 8402 TED 1 1 EEE & VI Solid State Drives EE 6601 SSD 2 3 LEE & VI Power Electronics & Drives Lab PEDLAB 3 EE 6611 2 ELE & IV Technical Seminar EE 8412 TSLAB 4 EEE & IV ۱ Library Hour LIE 5 20 TOTAL Authorized by Verified by Prepared by Lett. C. Jak Sign: Name: noigal -HD/NE Time Table Incha ge ----To a 2 Mathia a la cara 36833.344

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Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 633 056, Tirupur (Dt).





III YEAR NAME LIST

Department : Electrical and Electronics Engineering

S.No.	Reg. No.	Name of the Student
1.	732416105001	AMSAVENI S
2.	732416105002	ARIHARANP
3.	732416105003	BASKAR S
4.	732416105004	BOOPATHI S
5.	732416105005	DHARANI D
6.	732416105006	HARISH D
7.	732416105007	KALLALAHAR K S
8.	732416105009	KARTHIKEYAN V
9.	732416105010	KIRUBHAKARAN R
10.	732416105011	NAVEENKUMAR M
11.	732416105012	NAVEENKUMAR R
12.	732416105013	PAVITHRA M
	732416105014	RAJESHKUMAR M
14.	732416105015	TAMILSELVAM G
15.	732416105016	VANMATHIP
16.	732416105017	VIDHYA V
17.	732416105301	МАЛТН
18.	732416105302	GUNASEKARAN S
19:	732416405501	M.CHANDHRAKUMAR

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 633 055, Tirupur (Dt).



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SASURIE College of Engineering ١

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Department of Electrical and Electronics Engineering SUBJECT INFORMATION RECORD

Department		: EEE
Subject		SOLID STATE DRIVES
Year		
Semester *	•	: <u>v</u>
Last year handled by		:
Percentage of Result (last year)		:
Quality Objectives		: To understand Steady State operation
		and transient dynamics of motor load.

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REFERENCES:

	Prepared By	Approved By
Sign:	S. D.f.A	22/
Name:	S. Deepika	
	Faculty	HD
Cerestada		Dr. M. VIJAYAKUMAR ME., Ph PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt),



S DEEPIKA EEE

aculty Name

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URIE

(Accredited by NAAC, Under 2f and 12B status) LESSON PLAN

> Designation Assistant Professor Semester/ Year Vi / III

> > 1

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18.2.19

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: EE6601 /SOLID STATE DRIVES

ubject / Code : 2018 2019 ademic Year Proposed No. Date Actual Period TA Ref **Details of Topic Covered** Paniadi Data Remark: UNIT I DRIVE CHARACTERISTICS 19/12/2018 1,4 Electric drive Introduction 1 1 19.12.18 1 20/12/2018 typical load torque characteristics 1 1 4,6 22.1218 22/12/2018 4.6 steady state stability 1 1 2 24.12.18 2.1/12/2018 1 1 Component of load torque 1 4 9.1.19 26/12/2018 4 1 Classification of load torque 1 5 9.1.19 27/12/2018 1 multi quadrant Dynamics acceleration, deceleration 1 7 20.12.18 1 27/12/2018 .1 1 multi quadrant Dynamics - starting & stopping 7 20.12.18 29/12/2018 6 1 1 Equations governing motor load dynamics 4 19.12.18 1 1 31/12/2018 6 Selection of motor 5 27.12.18 UNIT II CONVERTER / CHOPPER FED DC MOTOR DRIVE 1 1 2/1/2019 1 Introduction steady state analysis 6_ 24.12.18 1 1 2/1/2019 -1 Single phase half controlled bridge converter control of DC seperately excited motor 29.12.15 1 1 3/1/2019 4 Single phase fully controlled bridge converter control of DC seperately excited motor 6 29.12.18 1 1 Three phase half controlled bridge converter control of DC seperately excited motor 2 5/1/2019 4 31.12.18 1 1 6 Three phase full controlled bridge converter control of DC seperately excited motor 31.12.15 5/1/2019 6 t 1 4 2.1.19 6/1/2019 2 Time ratio and current limit control 1 1 2 7.1.19 6/1/2019 1 Chopper Fed DC drives first and second quadrant operation 1 1 6 7.1.19 a Four quadrant operation 19/1/2019 1 1 5 3.1.19 Chopper control for seperately excited DC motor 6 19/1/2019 ١ 1 2 3.1.19 Chopper control for seperately excited DC series motor 2 21/1/2019 1 5.1.19 1 6 Problems 2 1/21/2019 UNIT III INDUCTION MOTOR DRIVES 1 1 6 21.1.9 Introduction to induction motor 28/1/2019 2 1 1 6.2.19 Stator voltage control 4 28/1/2019 2 10

30/1/2019	7.1	energy efficient drive - Problems	-	1.	4	6.2.19
1/1/2019		energy efficient drive and the action of the and the	1	1	4.	6.2.19
			1	1	5	7.2.19
2/2/2019	1	w/f control	1	1	1	9.2.18
2/2/2019	6	constant airgap flux			9	
12/2010	6 C	field weakening mode			L	11.2.1
	Salar Salara	current led drive			2	13.2.19
		voltage fed crive	1	1	4	13.2.19
2173F3			1	1	6	16.2.19
1919014		closed loop control	+	1	6	16.2.A
12/2019		UNIT IV SYNCHRONOUS MOTOR DRIVES	<u>les</u> Sale		Constrained Constrained	

introduction to synchronous motor is roots 1

- Sandraha Baran Marina (122)

the Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).



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(Arcredited by NAAC, Under 21 and 128 status) LESSON PLAN

Ity Name	Stripping
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"et / Code	: 116691 /50110

emic Year : 2018-2019

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Date	Period	Details of Topic Covered	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		Starich	Crito :
1/2/2019	6	V/I control of synchronous motor +		1	16	12.2.14
3/2/2019	2	self control of synchronois modul			2	4.3.17
1/2/2019	4	Margin angle control		1	6	4.3.7
6/2/2019	1,4	power facto: rontrol		1	12	14.3.19
8/2/2019	5	permanent magnet synchronous motor	1	1-1	6	1, 2.19
	• • • •	UNIT V DESIGN OF CONTROLLERS FOR DRIVES				
1/2/2014	14	Frances function for OC motor / loar and converter		1.	<u> </u>	6.3.11
5/272-19	•	devea lone - entrol with fram of any speed feerback		-	4.	6.2.17
1/3/2019	.1	armatore vertage reetror			11	6.3.11
1/2019	6	field weakeeing mode		1	1 5	7 3.17
1/ 1/2019	2,4	Design of controllers	·!			7.3.4
/3/2019	4,4	current controller		1	1	9.3.19
//3/2019		speed costrolle.		1	1	7 9.3.19
9-3/2019	4.6	converter selection and characteristics		,	1 2	2,6 11.3.19

ence books (Ref):

aal K.Dubey, Fundamentals of Electrical Drives, Narosa Publishing House, 1992

nal & Bose, Modern Power Flectronics and AC Drives, Pearson Enucation, 2007 Gisbinan, Flectric Motor & Drives, Moneling, Analysis and Control, Prectice Hall of

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Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt). to the Mar

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SASURIE College of Engineering

Department of Electronics and Communication Engineering

TEST PLAN FOR SUBJECT

Subject : semester : Faculty. Year:

Semester : Department :

Actual Remarks s. Planned Conducted Description No. Date/Month Date / Month 21.1.2019 21.1.2019 to 1 Internal Test-I 281.2019 27.2.2019 27.2.2019 Internal Test II 2 15.3.2019 15.3.2019 **Model Examination** 3

a filmation had	Prepared By	Approved By
Signi	S.D.A.	warman and the second
Name:	S. Dappiles	and a man a
	Faculty	100
		Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

Department of Electronics and Communication Engineering

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RESULT ANALYSIS OF TEST & CORRECTIVE ACTION PLAN

Subject code & Name:	EE6601 & SSD	Date: $2/\rho$ $\cdot 2c$
Class:	III ya	Semester: VI
Exam details & date	INTERNALI & 21.1.2019	
Faculty	$\gamma = -\gamma b + -\delta$	
Number of students	: 19	
No. of students attended	:18	
No. of students absent	÷ 1	
No. of students passed	: 9	
No. of students failed	: 9	
Overall Percentage	50-1.	

RESULT DATA:

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				76.00	91-100	
Marks	0-25	26-50	51-75	76-90	91-100	
 No. of Students	1	5	5	6	١	and the second se

ente. Reference de	Prepared By	Approved By
Sign:	Soup	2 AR
Name:	Faculty	HD
		Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

G. Tamil Selvan

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SASURIE College of Engineering

Department of Electronics and Communication Engineering

RESULT ANALYSIS OF TEST & CORRECTIVE ACTION PLAN

Subject code & Name:	EE6601	SSD	Date: 1-3-2019
Class: III yn			Semester: 5
Exam dejails & date	•	INTERNAL	I L 27.2.2019
Faculty		:	
Number of students		: 19	
No. of students attended		· 8	
No of students absent		- n	
No. of students passed		ד	
No. of students failed		a 1	
Overall Percentage		: 88-1.	

RESULT DATA:

	Marks	0-25	26-50	51-75	76-90	91-100
4	No. of Students	-)	+1	3	3

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Sign	S D-4-	25	
Name:		V	
	Faculty	HD	

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt). SASURIE

SASURIE College of Engineering

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Department of Electronics and Communication Engineering

RESULT ANALYSIS OF TEST & CORRECTIVE ACTION PLAN

Subject code & Name:	FEbbol	CI 22 3	Date: 18-3.2017
Class: IVy			Semester: VI
Exam details & date		MODEL 2	15.3.2019
Faculty		1	
Number of students		: 19	
No: of students attended		: 18-	
No. of students absent		:)	
No. of students passed		:7	
No. of students failed		: 11	
Overall Percentage		64 -1	

RESULT DATA:

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Marks	0-25	26-50	51-75	76-90	91-100
 No. of Students	8	2	2	6	-

148 (144) 148 (144)	Prepared By	Approved By
Sig	n: S.D.Fr	
Na	me:	
1. 	Faculty	HD

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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SASURIE Departme	nt of Electronic	Colle	SURIE gra of Engineering tion Engineering	1
			IVE ACTION PLAN	
Subject code & Name:	EE 6601	CI 22 9	Date: 18-3-2017	,
Class: IVy			Semester: <u>Vi</u>	L.
Exam details & date		MODEL S	15.3.2019	
Faculty		:		
Number of students		: 19		
No: of students attended		: 18		
No. of students absent		:)		.М.
No. of students passed		:7		
No. of students failed		: 11		
Overall Percentage		64 - 1.		

RESULT DATA:

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Marks	0-25	26-50	51-75	76-90	91-100	
No. of Students	8	2	2	6	-	
		and the second			1	

1 1980	Prepared By	Approved By
Sign:	SDD	
Name:		HD
a mittal	Faculty	

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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SASURIE College of Engineering

YEAR: IT

(Accredited by NAAC)

CORRECTIVE ACTION REPORT

DEPARTMENT: EEE

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SEMESTER: VI SUBJECT: SSD Remarks Deadline . Corrective **Root Cause** S.No Internal Test Percentage date Action (Metrics) of marks Slip Tests Absteas helped studenticus adviced 1 50 -1. to write I to score Retest Marks. Slip Test Refests 88-1. helped students contl be 2 Neve marks . Corducted 11 to score Due to Re-test irregular will be Students conducted. 111 644 3 (Model Examination)

Sign: S-D.f.A. 98 Name: S.D.copika Faculty HD		Approved By	Prepared By	
1 AND A DECEMBER OF A DECEMBER		98-1-	S-D-bl	Sign:
Faculty		Н	N 147	Name:
Marine Course of management of the second	Λ	1	Faculty	

PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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Department of Electrical and Electronics Engineering

RIE of Engineering

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QUALITY OBJECTIVE MONITORING RECORD

Department: Electronical and Electronica Engineering

: [1] Year

·VI Semester

: Solid State Drives Subject

•						Mo	del	
		Interna	alTest-l	Interna	alTest-II	Examination		
S.No	Quality Objective	Expecting	Obtained result	Expecting result	Obtained result	Expecting result	Obtained result	
1	>=80%	7=80.1.	80.1	>= 80.1.	& \$-1.	>=&01.	64-7.	
				1 = 1				

	Prepared By	Approved By
Sign:	South	25-1
Name:	S. Deepile Faculty	HD
		Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL
		SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).



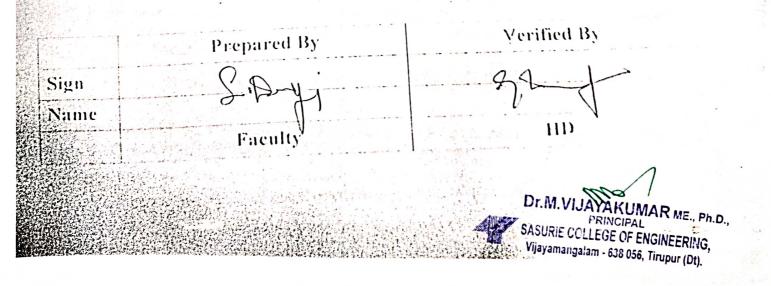
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(ACCREDATED BY NAACALISTED UNDER 2F AND 12B STATUS) ASSIGNMENT SCHEDULE

DEPARTMENT : ELECTRICAL AND ELECTRONICS ENGINEERING YEAR/SEM

ACADEMIC YEAR :2018-2019

S.NO	PARTICULARS	DATE
1	ASSIGNMENT - 1	7.1.2019
2	ASSIGNMENT - 11	11.2.2019
3	ASSIGNMENT - III	9.3.2019



Register Number:

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SASURIE

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College of Engineering

Course codeEE6601Regulation2013YearIII		Course Title		Date/Session 15.03.2019 Marks Solid state drives				
		2013		Duration	3 Hours	Academic Year	2018-2019	
		111		Semester	VI	Department	EEE	
COURSE	COUT	COMES	;			and an a second s		
CO1:	Ability to understand and suggest a converter for solid state drive.							
CO2:	Abil	ity to sele	ect suitabil	ity drive for the gi	ven application			
CO3:	Abil	ity to stud	ly about th	he steady state ope	ration and transient of	lynamics of a motor le	bad system.	
QU4:					verter/chopper fed d			
CO5:					rmance of AC motor		-	
CO6:	Abili drive	ity to ana	lyze and d	esign the current a	ind speed controllers	for a closed loop solid	d state DC mot	

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Q.No.	Question	CO	BT
	PART A		
	(Answer all the Questions $10 \ge 20$ Marks)		
1	What is meant by electrical drives?	COL	R
2	Mention the parts of electrical drives.	COI	R
3	Define Positive and negative motor torque.	CO2	R
4	What are the types of control strategies in DC chopper?	CO2	R
5	What is the advantages of using PI type speed controller?	CO3	R
6	What is commutator Less Motor(CLM)	CO3	R
7	List the advantages of squirrel cage induction motor	CO4	R
- 8	List the commonly used synchronous motors in industry.	CO5	R
O y	What are the advantages of cycloconverter?	CO5	R
10	How is PWM inverter supplied inVSI fed synchronous motor.	CO6	R
52	PART B		
· · ·	(Answer all the Questions $5 \ge 13 = 65$ Marks)		
11a	Write the equation governing the motor load dynamics.	COI	R
	Derive the equation for equivalent moment of inertia and load torque of loads with		
	translational motion.		
	OR		
11b	Illustrate the conditions of electrical drive from its heating and cooling curves and	COI	T
<u> 1</u>	explain the different classes of motor duty.		
			A
12a	Describe the steady state analysis of ongle phase tully controlled converter separately	CO2	C
	excited de motor drive in continuous and discontinuous modes		
	OR	.L	
12b	Describe the steady state analysis of three phase fully controlled converter separately	CO2	TO
120	excited de motor drive in continuous and discontinuous modes	1102	C
13a	Exclain construction and working principle of three phase induction motor	. cos -	-
129	Explain construction and working principle of three phase induction motor ULAYAKUM	AR ME. P	h.D.
	Dr.M.VIJAYAKUMAR ME, Ph.D., PRINCIPAL PRINCIPAL ALL SASURIE COLLEGE OF I	NGINFERING	
1	SASURIE COLLEGE OF ENGINEERINGiayamangatam - 633 05	6. Tirupur (Dt)	
	Vijayamangalam - 638 056, Tirupur (Dt).	, ioth	

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6. Versiellentingen denne die statigkenterplateere name entsterenteringe entstehende alle anderen sich die	1	-
OR		-
Explain the closed loop speed control of VSI fed & CSI fed induction drive	6115	1 .
Explan the concept of open loop V/I control of synchronous motor	1111	
OR		1
vplain self controlled mode of operation of synchronous motor	1 1 1 1	1
Aplain vector control of Permanent Magnet synchronous Motor	cas	1-
OR	-	
Explain self control technique of synchronous motor with constant margin control	1 COL	1
PART C	and service is served	1
(Answer all the Ouestions 1 x 15 = 15 Marks)		
Derive the closed loop transfer function of converter fed separately excited DC motor	1 CTM	T A
OR		1.1
Explain the design procedure of speed controller.	1 cor	T e
	Explain the closed loop speed control of VSI ted & CSI fed induction drive Explain the concept of open loop V/l control of synchronous motor OR Synchronous motor Aplain self controlled mode of operation of synchronous motor Aplain vector control of Permanent Magnet synchronous Motor OR Explain self control technique of synchronous motor with constant margin control. PART C (Answer all the Questions 1 x 15 = 15 Marks) Derive the closed loop transfer function of converter fed separately excited DC motor	Explain the closed loop speed control of VSI fed & CSI fed induction drive Explain the concept of open loop V/I control of synchronous motor OR Value of operation of synchronous motor Aplain vector control of Permanent Magnet synchronous Motor OR Explain self control technique of synchronous motor with constant margin control. COS PART C (Answer all the Questions 1 x 15 = 15 Marks) Derive the closed loop transfer function of converter fed separately excited DC motor OR COA

5. J- Kala 19 Course Faculty

Course Faculty (Name /Sign / Date) S.DFEPIKA

Q. W 1412199

(Name /Sign / Date) Ma . G . ງວາກ y

Principal

(Name/Sign/Date) K · Pandiarajan

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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Internal Assessment Test Answer Book

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Name	M. Pavithra	~		I AIT	
Register Number	732416105013	Date/Session	15.8.2019	Department	EEE
Course code	EE6601	Course Title	Solid g	Hate Drives	
Internal Assessment Test IAT 1			IAT 2	IAT 3 Model	নি
Name and Sig	nature of the Invigil	ator with date	DIE	a TELEPOLAVAYA	-

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	Part	A		J	Part B/ Pa	rt C		in a bhain
Q. No.	1	Marks	Q. NO.	~	a	~	b	Total Marks
~~~~~			Q. NO.		Marks		Marks	
1	~	2	11	~	13			13
- 2	~	2	12		12			12
3	1	2	13				12	12
4	~	2	14			$\checkmark$	9	9
5	~	2	15			~	10	10
6	~	2	16			V	10	10
7	~	2				Gr	and Total	66
8	1	2	86				C	λ
9.	V	2				·	5.124	Tech
10 -	~	2				SDEEPIKA		
Total 20			Gran	Name and Signature and Total of the Examiner with d				

To be filled by the examiner Course Outcomes 2 1 3 4 5 Total 6 Marks allotted 17 17 17 17 15 00 17 17 16 10 IQAC Audit - Remarks Marks Obtained 8.6 16 11 14 12 Monks Verified ame and Signature Dr.M.VIJAYANUMAR MEL PRE-PRINDRM.VIJAYAKU AKUMAR ME., Ph.D., . . SASURIE COLLEGE OF ENGINEERINGICIPAL Vijayamangalam - 638 056, Tirupur (Dt).





## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

## **Assignment Question Paper**

Name of the Student: Gr . Ramil Selvan

AU Register Number: 73.2416105015

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Course code	FES601	Course Title	Solid state drives			
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Q.No	Questions	со
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Name and Signature of the Faculty Incharge

S. DEEPIKA

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Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).



## SASURIE College of Engineering

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

## Assignment Answer Sheet

Name of the Student: Of Tamil Selvan

AU Register Number: 732416105015

Γ		Assignment - 01		Date of Issue:	30,12,2018	Marks	10
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#### Mark Allocation

Rubrics	Marks Allocated	Marks obtained
Content Quality	6	6
Presentation Quality	2	.2
Timely submission	2	2
Total marks	10	10

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Name and Signature of the Faculty Incharge

S. DEEPIKA

Dr.M.VIJAYAKUMAR ME., Ph.D., SASURID COLLEGE OF ENGINEERING. Vijavamarigalam - 638 056, Tirupur (Dt).

Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayaniangalam - 533 056 Tirupur (Dt)