

SASURIE COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi. Affiliated to Anna University, Chennai

Near NH544, Coimbatore Bypass, Near Vijayamangalam Tollgate, Tirupur 638056

NAAC DOCUMENTS

QUALITY INDICATOR FRAME WORK

CRITERION - 1

CURRICULAR ASPECTS



INTERNAL QUALITY ASSURANCE CELL

SASURIE COLLEGE OF ENGINEERING





Criterion 1 Curricular Aspects	100
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1.1 Curricular Planning and Implementation (20)

1.1.1The Institution ensures effective curriculum planning and delivery through a well-planned and documented process including Academic calendar and conduct of continuous internal Assessment

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1	Contents - Course File
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3	Students Name List
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7	Test Plan for Subject
8	Result Analysis of Test
9	Corrective Action
10	Quality Objective Monitoring Record
11	Test Question Paper
12	Test Answer Sheet
13	Assignment Question Paper
14	Assignment Answer Sheet



· / Electronic Devices and CRAUSTE : ECE, 1. Departmen' Subject Code & Name: EC3353 . Class & Batch 11/2021-2025 Semester 111 REMARKS

CONTENTS - COURSE FILE

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6	Test Plan for the Subject	
7	Result Analysis	
8	Corrective Action Report	
9	Quality objective monitoring record	
10	Internal test mark sheet/Consolidated/	
11	Internal test question paper	
12	Model question paper with answer key	
- IJ	Sample Answer paper for all test(Min-3)	
14	Content beyond the syllabus	
15	Tutorial Class - schedule and content	
16	Assignment - schedule and paper	
17	PPT - handout	
18	Video - Animation - Soft copy	Martin Contractor
- 19	Question bank	
20	Sample university question papers(min 5 QP-recent exam)	
21	Personal Log book - Updated	
	News	
	Special Class if any. Approval letter. Schedule. content	
23	covered.	

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Name:	Mr. V. hoppiekessharen Faculty	And Then shere
	Facuity	Paset of 615 Technimer

Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056 Ti----

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w.e.f : 22.08.22

Department : ECE

CLASS TIME TABLE - Academic year 2022-23 ODD Sem

Year&Sem: II&III

Name of the faculty :Mr. V.GOPALAKRISHNAN - AP / ECE

	1	1		III	IV	The Marsh	V	VI	ALCONT OCA.		
DAY/ TIME	1 09.30s.m TO 10.15 s.m.	10.15a.m. TO 11.00a.m.	11.00s.m TO 11.10 s.m	11.10 a.m. TO 11.55 a.m.	11.55 a.m. TO 12.40p.m.	12.40 p.m. TO 1.20 p.m.	1.20 p.m. TO 2.00p.m.	2.00 p.m. TO 2.40p.m.	2.40 p.m. TO 2.50p.m.	2.50 p.m. TO 3.35 p.m.	3.35 p.m. TO 4.20 p.m.
MONDAY						States of		EDC			
TUESDAY					N. S. A.		EDC				
WEDNESDAY			BREAK		EDC	IUNCH			BREAK		
THURSDAY		EDC	BRE			3					
FRIDAY											
SATURDAY					En la			EDC		EDC	

S.No.	Subject Code	Name of the Subject	Class	No. of Hours
1	EC3353	ELECTRONIC DEVICES AND CIRCUITS	Mr.V.GOPALAKRISHNAN AP/ECE	6
ter a contraction		TOTAL		6

Authorized by Verified by Prepared by LUU Signe Dr.M.VIJAYAKUMAR Prof.T.MANICKAM Mr.V.GOPALAKRISHNAN Marne: PRINCIPAL HOD FACULTY

TSASURIE College of Engineering Vijavamangalam Tiruppur-638 056

Dr.M. VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING. Vijayamangalam - 038 056, Timpur (Dr)

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SASURIE College of Engineering

Academic Year 2022 - 2023 ODD Semester

STUDENT NAME LIST

Department : Electronics and Communication Engineering

Subject :	EC	3353-Electronic	Devices	and	Circuits
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SI. No	REGISTER NO.	NAME OF THE STUDENT	HOSTEL/DAYSCHOLER
1	732421106001	GOKUL.S	D
2	732421106002	ROKESH.P	D
3	732421106003	SABARI JOTHI.S	D

	Prepared by	Verified by
SIGN	1 Quering	q.J.
NAME	Mr.V.GOPALAKRISHNAN	Prof.T.MANICKAM
	FACULTY	HOD

Head of ECE Department SASURIE College of Engineering Vijavamangalam Tiruppur-638 056

Dr.M.VIJAYAKUMAR ME. Ph.D. SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt). PRINCIP

SYLLABUS

COURSE OBJECTIVES :

 To give a comprehensive exposure to all types of devices and circuits constructed with discrete components. This helps to develop a strong basis for building linear and digital integrated circuits

. To analyze the frequency response of small signal amplifiers

 To design and analyze single stage and multistage amplifier circuits. To study about feedback amplifiers and oscillators principles.

To understand the analysis and design of multi vibrators.

UNIT I SEMICONDUCTOR DEVICES

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PN junction diode, Zener diode, BJT, MOSFET, UJT -structure, operation and V-I characteristics, diffusion and transition capacitance - Rectifiers - Half Wave and Full Wave Rectifier, Zener as regulator.

UNIT II AMPLIFIERS

Load line, operating point, biasing methods for BJT and MOSFET, BJT small signal model – Analysis of CE, CB, CC amplifiers- Gain and frequency response –MOSFET small signal model– Analysis of CS, CG and Source follower – Gain and frequency response- High frequency analysis.

UNIT III MULTISTAGE AMPLIFIERS AND DIFFERENTIAL AMPLIFIER

Cascode amplifier, Differential amplifier – Common mode and Difference mode analysis – MOSFET Input stages – tuned amplifiers – Gain and frequency response – Neutralization methods.

UNIT IV FEEDBACK AMPLIFIERS AND OSCILLATORS

Advantages of negative feedback – Voltage / Current, Series , Shunt feedback Amplifiers – positive feedback–Condition for oscillations, phase shift – Wien bridge, Hartley, Colpitts and Crystal oscillators.

UNIT V POWER AMPLIFIERS AND DC/DC CONVERTERS

Power amplifiers- class A-Class B-Class AB-Class C-Power MOSFET-Temperature Effect- Class AB Power amplifier using MOSFET –DC/DC convertors – Buck, Boost, Buck-Boost analysis and design.

Head of ECE IN

College of Engineering

Dr.M.VIJAYAKUMAR ME, Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (0t).



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LESSON PLAN

esignation: Assistant Professor emester/ Year: III / II

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Faculty Marine	I MA. V.GOPALARRISHMAN	Designatio
Department	:64	Semester)
Subject / Code	EC 3353 - ELECTRONIC DEVICES AND CIRCUITS	
Academic Year	: 2022-2023(000)	

Prop		rel	I		1.	Actual	110D size	
S.Ne	Date	Period	Details of Tople Covered	TA	Hel.	Date	Period	11-715 4164
	- Change		UNIT 1 - SEMICONDUCTOR DEVICES					
1	10.08.2022	8	PN junction diode and Zener diode.	1	1	22.08.2	6	
2	11.08.7022	5	BJT-structure, operation and V-I characteristics.	1	1	208 m	6	2.19
3	11.08.2022	6	MOSFET-structure, operation and V-I characteristics.	1	1	2208 22	5	
4	12.08.2022	3	UJT-structure,operation and V-I characteristics.	1	1	23.08.n	5	
5	13.08.2022	4	Diffusion.	1	1	24.08.20	4	
6	16.08.2022	5	Transition capacitance.	1	1	24.08.22	4	
7	15.08.2022	6	Rectifiers.	1	1	25:08-22	3	
8	17.08.2022	8	Half wave and full wave rectifiers.	1	1	25.08.m	21	t.
9	18.08.2022	5	Zener as regulator.	1	1	20,08-20	4	T
1.40			UNIT II - AMPLIFIERS		346		1	
10	22.08.2022	1	Load line and operating point.	1	1	27.08.22	6	
11	23.08.2022	5	Biasing methods for BJT and MOSFET.	1	1	23.08.22	6	
12	23.08.2022	6	BJT small signal model.	1	1	27.08 22	7	
13	24.08.2022	8	Analysis of CE,CB,CC amplifiers.	1	1	29.08.22	6	a ser la la
14	25.08.2022	5	Gain and frequency response.	1	1	29:00 24	6	
15	26.08.2022	3	MOSFET small signal model.	1	1	29 08.20	6	
16	27.08.2022	4	Analysis of CS,CG and source follower.	1	1	20.18.21	5	
17	29.08.2022	1	Gain and frequency response.	1	1	20 08. 22	5	Jr.
18	30.08.2022	5	lligh frequency response.	1	1	30.08.22	5	0
12.9			UNIT - III MULTISTAGE AMPLIFIERS AND DIF	FERE	TIAI	. AMPLIFIER	S	New Colors
19	01.09.2022	5	Cascode amplifier.	1	1	01.09.22	5	
20	02.09.2022	3	Differential amplifier.	1	1	02.09.2	3	
21	05.09.2022	1	Common mode analysis.	1	1	05.09.22	1	145.5
22	06.09.2022	5	Difference mode analysis.	1	1	ct. 09 22	5	
10	07.09.2022	8	MOSFET input stage.	1	1	07.09.20	8	

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LESSON PLAN

SASURIE,

Faculty Name Department : Mr. V.GOPALAKRISHNAN

Designation: Assistant Professor Semester/ Year: III / II

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Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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Subject / Code Academic Year

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EC 3353 - ELECTRONIC DEVICES AND CIRCUITS

Year : 2022-2023(ODD)

	Propos	ed		1.	1	Actual		HOD sign	
S.No	Date	Period	Details of Tople Covered	TA	Ref.	Date	Period		
			UNIT I - SEMICONDUCTOR DEVICES		_				
1	10.08.2022	8	PN junction diode and Zener diode.	1	1	22.08 m	6	and and all all all all all all all all all al	
2	11.08.2022	5	BJT-structure, operation and V-I characteristics.	1	1	208 n	5		
3	11.08.2022	6	MOSFET-structure, operation and V-I characteristics.	1	1	22,08 n	5		
4	12.08.2022	3	UJT-structure, operation and V-I characteristics.	1	1	23.08.n	E		
5	13.08.2022	4	Diffusion.	1	1	24.08.n	4		
6	16.08.2022	5	Transition capacitance.	1	1	24-08.22	4		
7	16.08.2022	6	Rectifiers.	1	1	2E:08-22	ã.		1
8	17.08.2022	8	Half wave and full wave rectifiers.	1	1	25.08.22	21	t.	48
9	18.08.2022	5	Zener as regulator.	1	1	20.08.2	4	61	pr
1 mar		2000	UNIT II - AMPLIFIERS		_				1
10	22.08.2022	1	Load line and operating point.	1	1	27-08-22	6	1122	
11	23.08.2022	5	Biasing methods for BJT and MOSFET.	1	1	27.08.22	6	Sec. Star	
12	23.08.2022	6	BJT small signal model.	1	1	27.08 22	F	A REAL PROPERTY	+
13	24.08.2022	8	Analysis of CE,CB,CC amplifiers.	1	1	29.08.22	6		-
14	25.08.2022	5	Gain and frequency response.	1	1	29:00 22	6		-
15	26.08.2022	3	MOSFET small signal model.	1	1	29 c8.n	6		-
16	27.08.2022	4	Analysis of CS,CG and source follower.	1	1	20 18.22	5		-
17	29.08.2022	1	Gain and frequency response.	1	1	20 08. 22	5	12	18
18	30.08.2022	5	High frequency response.	1	1	30.08. 22	5	10	X
			UNIT - III MULTISTAGE AMPLIFIERS AND DI	FFERE	NTIA	L AMPLIFIE	RS	1	_
19	01.09.2022	5	Cascode amplifier.	1	1	01.09.22	5		-
20	02.09.2022	3	Differential amplifier.	1	1	02.09.2	3		-
11	05.09.2022	1	Common mode analysis.	1	1	05.09.2	A CONTRACTOR		-
12	06.09.2022	5	Difference mode analysis.	1	1	02.09 21	-		-
3	07.09.2022	8	MOSFET input stage.	1	1	07.09 21	8	10000	

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Mr. V.OOPALAKRISHNAN

Faculty Name



TEC 3353 - ELECTRONIC DEVICES AND CIRCUITS

Subject / Code Academie Year

12021-2021(000)

	Proposed		Details of Tople Covered	AT	Ret	
ia –	Date	Period		1	-	Date
4	08.09.2022	3	Luned amplifiers.		-	43.00 P
	10.09.2022	4	Single tuned amplifiers.	1	1	00 09 22 10 09 22
0	13.09.2022	3	Gain and frequency response.	1	1	13.09 2
17	13.09.2022	0	Neutralization methods.	1	1	B.a.n
			UNIT IV - FEEDBACK AMPLIFIERS AND O	SCILLAT	ORS	In
28	14.09.2022	8	Advantages of negative feedback.	1	1	Wag n
29	15.09.2022	5	Voltage and current series feedback amplifiers.	1	1	15.09 m
30	15.09.2022	3	Voltage and current shunt feedback amplifiers.	- 1	1	16.09.2
31	19.09.2022	1	Positive feedback.	1	1	19.09 2
32	20.09.2022	6	Condition for oscillations.	1	1	20.09.22
11	28.09.2022	8	Phase shift oscillator.	1	1	22 50
34	29.09.2022	5	Wien bridge oscillator.	1	1	28.09 2
35	30.09.2022	3	Hartley and colpitts oscillator.	1	1	30 09 22 3
36	01.10.2022	4	Crystal oscillators.	1	1	01 10.2.1.
149.919			UNIT V POWER AMPLIFIERS AND DC/DC	CONVER	TERS	-74
37	06.10.2022	5	Power amplifiers.	1,3	2	cb 10.21 F
38	07.10.2022	3	Class A and class B amplifiers.	1	2	C7. K. 21 6
39	08.10.2022	4	Class AB and class C amplifiers.	1	2	08.tc. 22 4
40	10.10.2022	1	Power MOSFET.	1,3	2	10.10 22 1
41	11.10.2022	2 5	Temperature effect.	1,3	2	11 10.22 5
42	19.10.2022	2 8	Class AB power amplifier using MOSFET.	1,3	2	1910.12 8
43	20.10.202	2 5	DC/DC convertors.	3	2	20 10. 2 5
44	26.10.202	2 8	Buck boost.	3	2	28.10 22 8
45	27.10.202	2 3	Buck boost analysis and design.	3	2	27.10.12 3

LESSON PLAN

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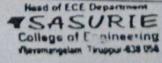
1. Donald A.Neaman, "Electronic circuit analysis and design". Tata Mcgraw hill, 3 rd edition. 2010

2. D.Schilling and C.Belove, "Electronic circuits". McGraw hill, 3 rd edition. 1989

2. Muhammad H.Rashid, "Power electronics" pearson education /PHI.2004

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Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).

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Designation: Assistant broken



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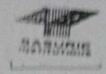
SUBJECT INFORMATION RECORD

Department	ECE .
Subject	Electronics Devices and Aquits.
Year	: <u></u>
Semester	: 111
Last year handled by	*
Percentage of Result (last year)	: - thop 90 %.
Quality Objectives	To produe more than 90%. To produe more than 90%. Menut in anna university exam

Reference Book:

: 1. David. A. Bell "Electronic Devius and Whits" 1. David. A. Bell "Electronic Devius and Whits of the court of the c

	Prepared By	Approved By
Sign: Name:	Mr. V. GOPALAKRISHNAN	Prot T MANICUPM HOD
	Faculty	Head of ECE Department MSASURIE College of Engineering Vijayamangalam Tiruppur. 638 (56
	Dr.M. VIJAYAKUMAR ME., Ph PRIMCIPAL SASURIE COLLEGE OF ENGINEERING Vijayamangalam - 638 056. Tirupur (Dt).	n.D.,



TEST PLAN FOR SUBJECT

subject Mathonic Devices and wacusts

Faculty: Mr. V. Glopab. houtehoon Year. TT

Semester 1 III

Department : ECE

S. Ne.	Description	Planned Date/Month	Actual Conducted Date / Month	Remarks
1,	Internal Tat -I	17.10.22	17.10.22	-
2.	Internal Tost - II	19.12.22	19 12.22	-
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1972	Prepared By	Approved By
Sign:	V. Quint	42
Name:	Mr. V. GOPALAKELSANAN Faculty	M A JULINAM. P. LONG
	Dr.M.VIJAYAKUMAR ME. P.D. PRINCIPAL SASURIE COLLECE OF ENGINEERING.	Head of ECE Department MSASURIE College of Engineering Vijeremangelan Teuppur 638 054



RESULT ANALYSIS OF TEST

Subject Electronic	Devius & clauts	Date : 17. 10.22
Class I		Department ECE
Semester : 111		1 10.22
Exam details & date	Fortestral	Text -1 2 17. 10.22
Faculty	: Hr. V.	of gralatorishnan
Number of students	: 0 <i>9</i>	
No. of students attended	: D3	
No. of students absent	: _	
No. of students passed	: 03,	
No. of students failed	÷ -	
Percentage of failures	: -	

RESULT DATA:

Marks	- 0-25	26-50	51-75	76-90	91-100
No. of Students	-	-	03	-	-

Prepared By	Approved By
V. Orminy	Y
Hr.V. GOPALPKESSANAN	Prof. T. MANJCKAM.
Faculty	mass of the disabilitient
Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dr)	CribUning



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RESULT ANALYSIS OF TEST

subject Electronic	Devices & liscuit Date : 19.12. and Department : EEF.
Class II	: Intornal Exam - II 2 19.12.22
Semester III	Internal Exam - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -
Exam details & date	: Internal Each Englassishnan. : Mr. V. Gopalabrishnan.
Faculty	: 03
Number of students No. of students attended	: 0.3
No. of students absent	: -
No. of students absent No. of students passed	: 03
No. of students failed	
No. of subcertage of failures	

RESULT DATA:	20	5-50 51-75	76-90	91-100
Marks	0-25 20	03	1 -	-
No. of Students		-		1

Approved By Prepared By V. Orwing Mr. V. GOPAIAKRISANAN Proj. T MANTCHAM Name SASURIE Facultyo College of Engineering Majamingalam Taruppu 838.055 DE.M. VIJAYARUMAR ME., Ph.D. PRINCIPAL SASURIE COLLEGE OF ENGINEERING,

Vijayamangalam - 638 056, Tirupur (DI).



CORRECTIVE ACTION PLAN REPORT

Dept. ECE

Year : 71.

Electronic Devices and U'sailts Subject Semester: ill

S.No	Internal Test	Precentage of Marks	Root Cause (Metries)	Corrective Action	Deadline Date	Remarks
I	Internal Text - I	100%.		-	-	-
2	Interload Tear II	100 %.	-	-	-	-

Prepared By Approved By V. Quun Sign: Name: Mr. V. GOPALAKRSSANAN Prot J MANJ CLOP M Faculty HOD Head of ECE Department **V**SASURIE College of Engineering Vijayamangalam Tuupput-638 056 Dr.W. Victory PRINCIPAL the state is a SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056. Tirupur (Dt).



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QUALITY OBJECTIVE MONITORING RECORD

Department :	EC	£ _	-1.1					
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Year

Semester : 111

subject Electronic Devius and uguits.

Omality		Internal Test-I			al Test-II	Model exam	
S.No	Quality Objective	Expectin g result	Obtained result	Expecting result	g Obtaine d result	Expectin g Result	Obtained
1.	> 907	100 %.	100%.	90 %	1007.	-	-
							1
							1
Carlos Carlos	anter de la companya de la company	Prepa	ared By		0	Approved By	
Sign:	int			de			
Nam	e: Mr.	V. GOPAL	ARRESA	NAN	Prot . T	MPNI C HOD	h AM
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Register Number: SASURIE 1110 College of Engineering SASURIE 50 Marks 17.10.2022 Date/Session **Internal Test-I** Course **Electronic Devices and Circuits Course** Title EC3353 code Academic 2022-23 1.30 Hours Duration 2021 Regulation Year ECE Department Semester Ш II Year **COURSE OUTCOMES** To give a comprehensive exposure to all types of devices and circuits constructed with discrete CO1: components. This helps to develop a strong basis for building linear and digital integrated circuits To analyze the frequency response of small signal amplifiers CO2: To design and analyze single stage and multistage amplifier circuits CO3: To study about feedback amplifiers and oscillators principles CO4: To understand the analysis and design of multi vibrators CO5: CO BTS Question Q.No. PART A (Answer all the Questions $10 \ge 2 = 20$ Marks) COI R What is mean by Zener breakdown? 1 U COL Draw the VI characteristics of PN junction diode. 2 COL R Define knee voltage of a diode. 3 COL R What is a rectifier? 4 What are the operating modes of MOSFET? COL R 5 CO2 R Define early effect. 6 CO2 AN List out the types of rectifiers. 7 CO2 R Mention few applications of UJT 8 What are the applications of Zener diode? CO2 R 9 What is drift current? CO2 R 10 PART B (Answer all the Questions 2 x 17 = 30 Marks) Explain the working principle of a PN junction diode with necessary diagrams. COL U 11a OR U 11b Explain about the function of half wave and full wave rectifiers. COI Explain how the Zener diode can be used as voltage regulators. CO2 U 12a OR Briefly discuss on the following structures: BJT and UJT. C 12b CO2 13/10/2022 V. Burghol2 Course Faculty Brineipal Dr.M.V.JavavSign (Name /Sign / Date)

(Name /Sign / Date)

T. MANICKAM. AN SASURE COLLEGE TITAYAKUMAR

V. Gopalakrisman.



Internal Assessment Test / nswer Book

Name	S. Sabari	Jothi		Year/ Semester/Sect	ion I& W
Register Number	73.2421 106 00 3	Date/Session	H-10-22 & PN	Department	ECE
Course code	Ec3853	Course Title	Electronia	c devices and	cincuids
Internal Asse	essment Test	IAT 1	IAT 2	IAT 3	Model
Name and Sig	gnature of the Invig	ilator with date	\$ 110/22	s. Babyrani.	

Part A								
0 N	1	M	O NO	1	a	1	b	Total Marks
Q. No.		Marks	Q. NO.		Marks		Marks	
1	1	1	11	1	ц			11
2	V	2	12	1	(0)			10
3	V	45	13			1	1.1.1	
4	V	2	14					
5	5	2	15					
6	1	1	16					
7	V	2				Gr	and Total	21
8	1	2						
9	~	14		38			. ~	X
10	~	2					(V. gopa	alakrishna
Total		17	Gra	and T	otal	of	Name'and the Examin	Signature her with date

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Course Outcomes	1	2	3	4	5	6	Total
Marks allotted	25	25	-	-	-	-	50
Marks Obtained	192	182	- 1	-	-	-	38.
Harris	Mark	s Veruf	hed			CT. MA	19/10/2022 WickAm) Signature C member



DEPARTMENT OF Electronics and Communication Engineering

Assignment Question Paper

	Assignmen	nt – 02	Date of Issue:	05.09.2022	Marks	10
Course code	EC3353	Course Title		vices and Circuits	1	
Year	II	Semester	III	Date of Submission:	15.09	2022

Q.No	Questions	СО
1	Wien - Bridge Oscillator.	C02
2	Derivation for Frequency of Oscillator.	
		CO2

2000/22 Name and Signature of the Faculty Incharge V. Gopalakrishnan.

9/2022

(T. MANICKAM)

Dr.M.VIJAYAKUMAR ME., Ph.D., PRINCIPAL SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).



DEPARTMENT OF Electronics and Communication Engineering

Assignment Answer Sheet

Name of the Student: P. Rokesh

AU Register Number: 732421106002

	Assignme	nt – 02	Date of Issue:	05.09.2022	Marks 10
Course code	EC3353	Course Title	Electronic Dev	vices and Circuits	
Year	П	Semester	111	Date of Submission:	15.09.2022

Q.No	Questions	СО
1	Wien – Bridge Oscillator.	CO2
2	Derivation for Frequency of Oscillator.	CO2

Mark Allocation

Rubrics	Marks Allocated	Marks obtained
Content Quality	6	6
Presentation Quality	2	2
Timely submission	2	2
Total marks	10	lo

Name and Signature of the Faculty In charge V. Gopalakrishnan.

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J. MANICKAM) Dr.M. VIJAYAKUMAR ME., Ph.D., SASURIE COLLEGE OF ENGINEERING, Vijayamangalam - 638 056, Tirupur (Dt).